

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 01997-254002	Application No. 09/377,372
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Arvind Mithal et al.	
		Filing Date August 19, 1999	Group Art Unit 2731

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AB	*EP 0329233	10/02/89	European Patent Office				
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	AG	Cook et al., "Formal verification of explicitly parallel microprocessors", March 5, 1999
	AH	Gupta et al., "Hardware-software Co-synthesis for Digital Systems", Computer Science Laboratory, Stanford University, 1993
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Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Notice of References Cited	Application/Control No. 09/377,372	Applicant(s)/Patent Under Reexamination MITHAL ET AL. DS	
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NON-PATENT DOCUMENTS

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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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